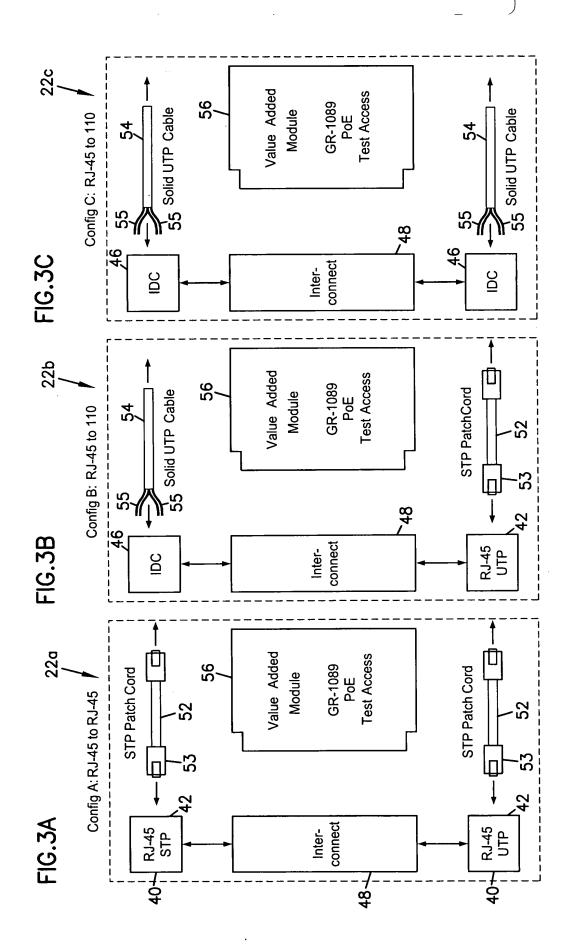
Inventor: COFFEY ET AL.
Docket No.: 2316.1834US01
Title: PATCH PANEL WITH MODULES
Serial No.: 10/814,107
Sheet 1 of 14 PATRICIANA JUL 1 9 200 00 00 Port \ 24 22a Inter-connect RJ-45 STP RJ-45 UTP ا ا Port 40 23 40 30 Inter-connect RJ-45 STP RJ-45 UTP 4 Inter-connect RJ-45 UTP RJ-45 STP Port 22 / ස 30 48 / 427 42 **24** <del>5</del>6 12 Panel 2RU 56 48 30 -30 Port \ 3 22a t | connect | RJ-45 UTP 40 RJ-45 STP Port 2 22a 5 Inter-connect RJ-45 UTP RJ-45 STP Port | 42 1 22a 8 23 connect RJ-45 UTP RJ-45 STP Inter-42 48 00 00 4

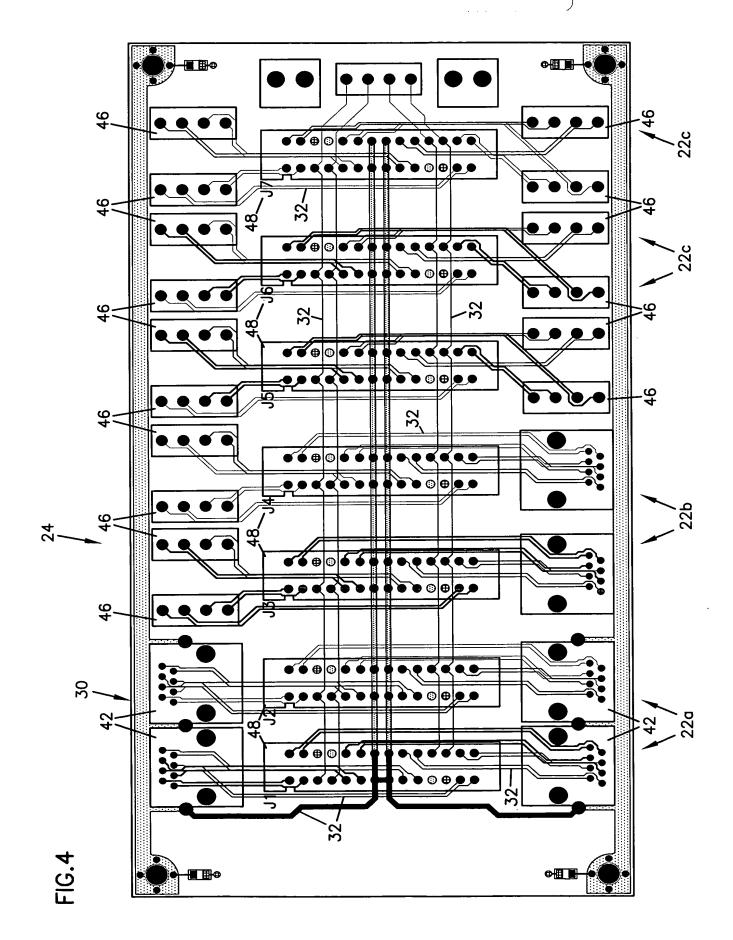
Inventor: COFFEY ET AL.
Docket No.: 2316.1834US01
Title: PATCH PANEL WITH MODULES
Serial No.: 10/814,107
Sheet 2 of 14 Inter-connect Value Added Module RJ-45 (TOP) 1 Port 22a <sub>24</sub> Value Added Module connect 1 Port 22a 23 RJ-45 (TOP) Module B Inter-Power connect Added Module RJ-45 (TOP) Value 22d 22 Inter-34 12 36 Rear Mounted Optional Modules Power and SNMP CPU Backplane SNMP CPU **2**6 28 56  $\frac{1}{\infty}$ 20 Value Added Module connect RJ-45 (TOP) Port 3 24 Inter-22a Module A  $\frac{1}{22a}$  Port  $\frac{2}{2}$ Value Added Module Power connect RJ-45 (TOP) Inter-Value Added Module Port connect RJ-45 (TOP) Inter-**FIG.2** 22a 4

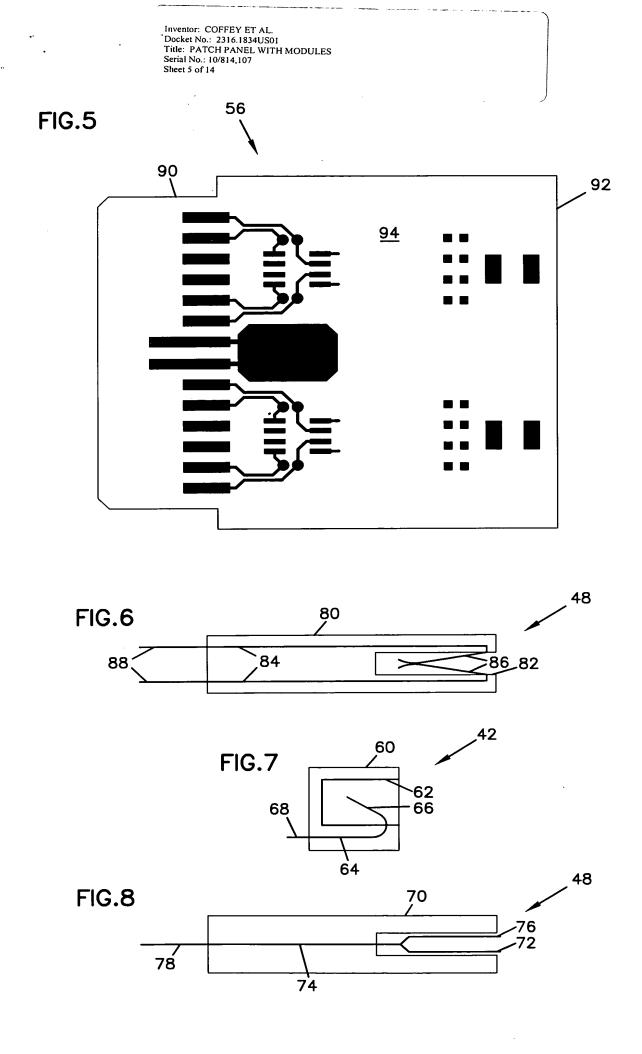
Jinventor: COFFEY ET AL.
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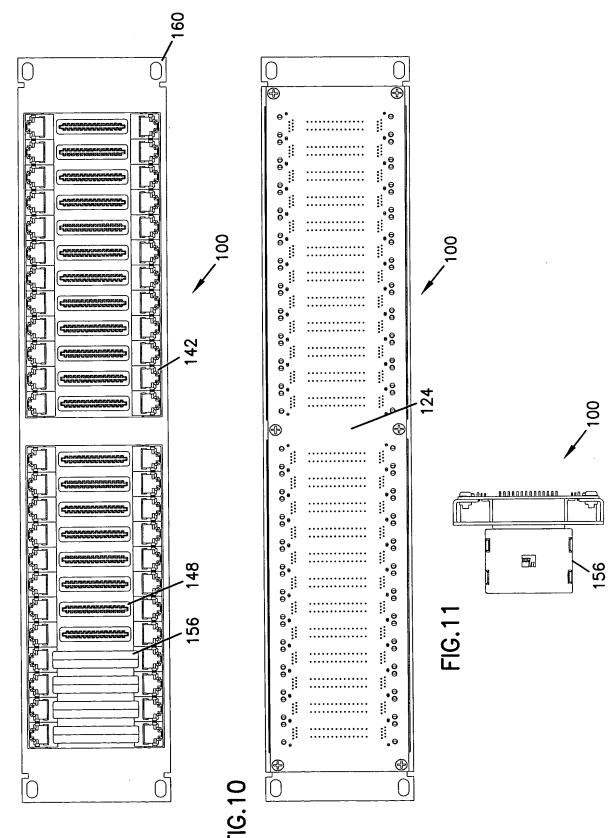
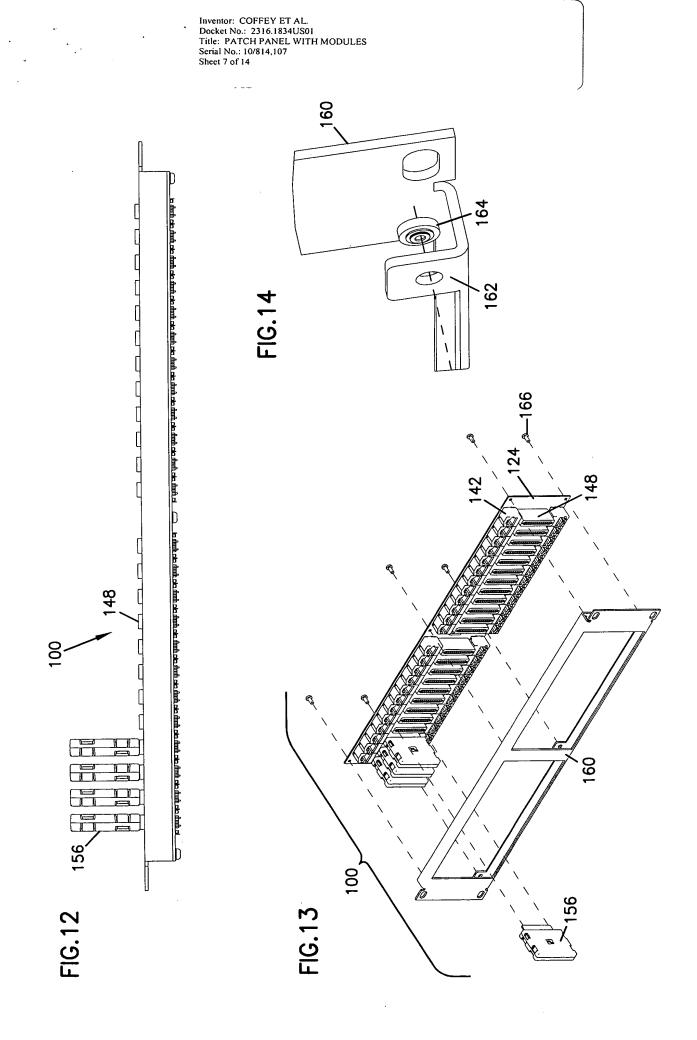


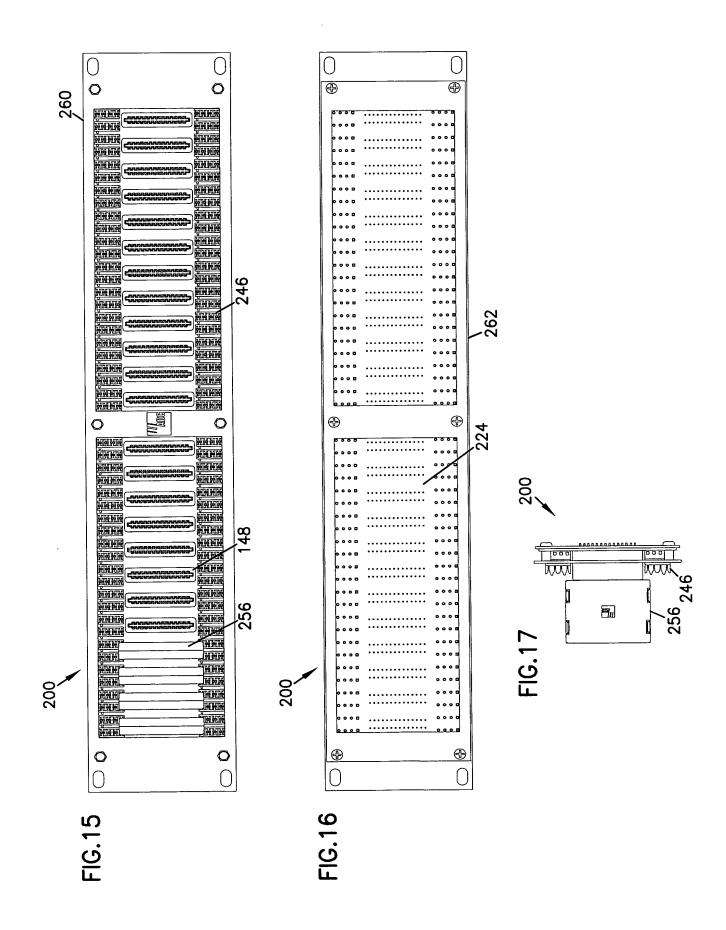
FIG.9

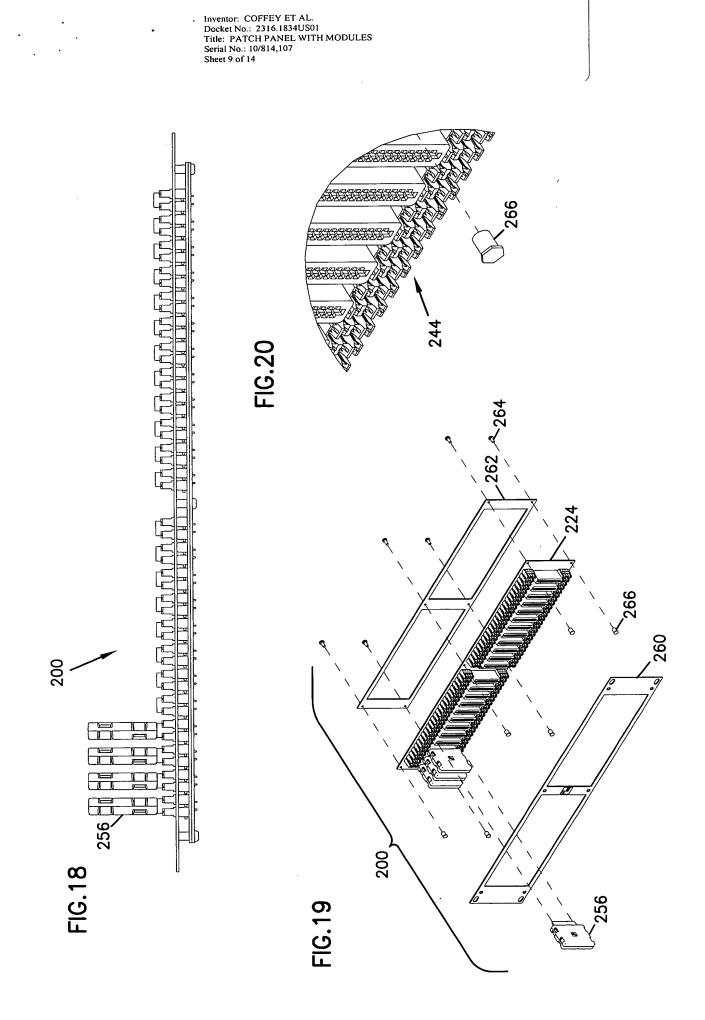


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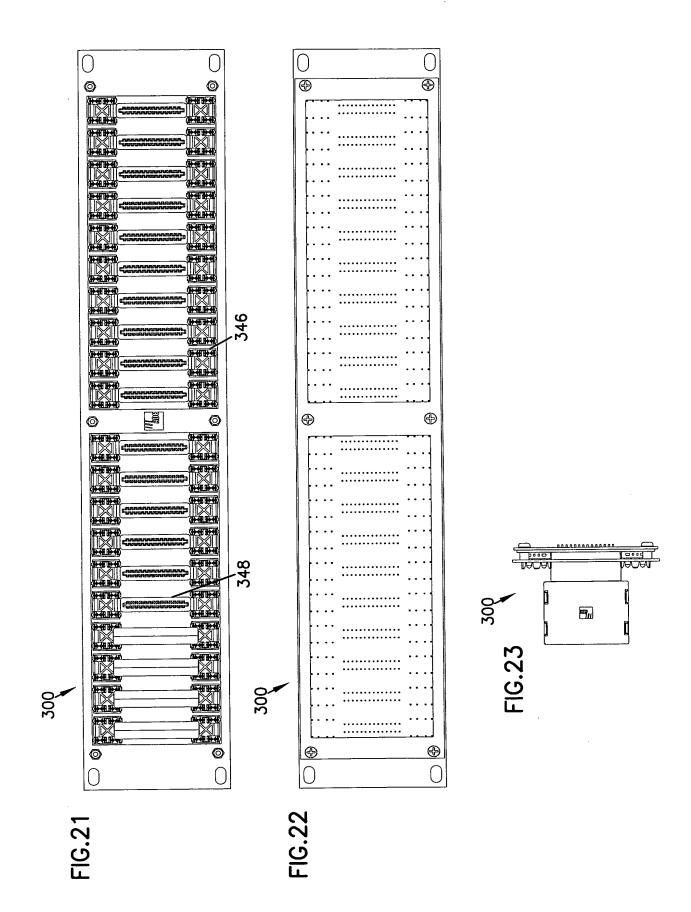
Sheet 8 of 14

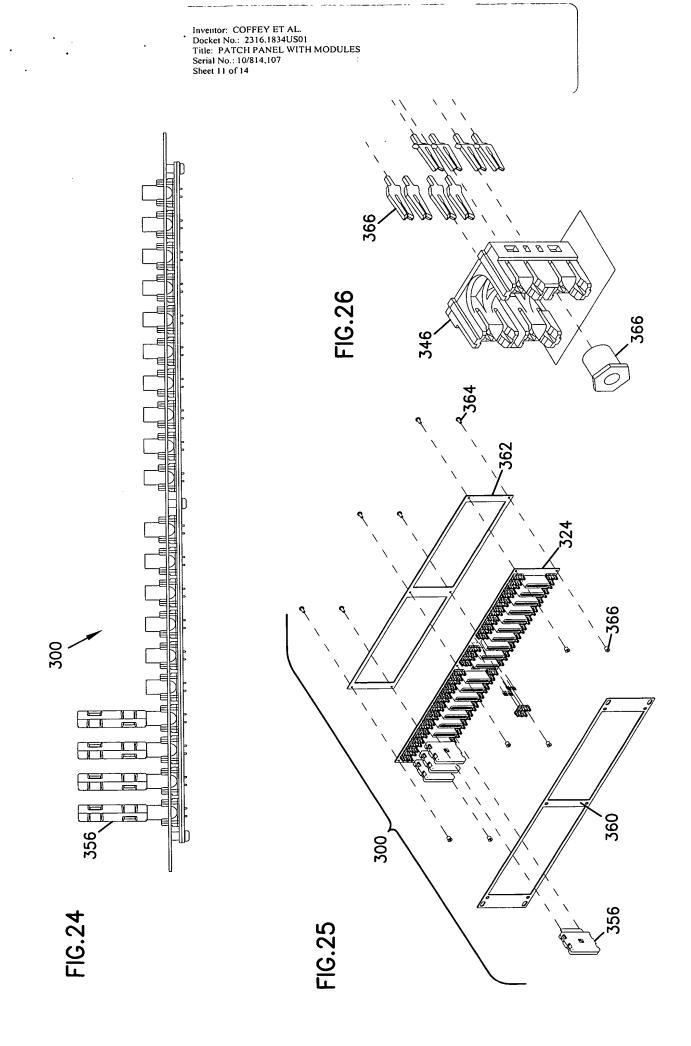




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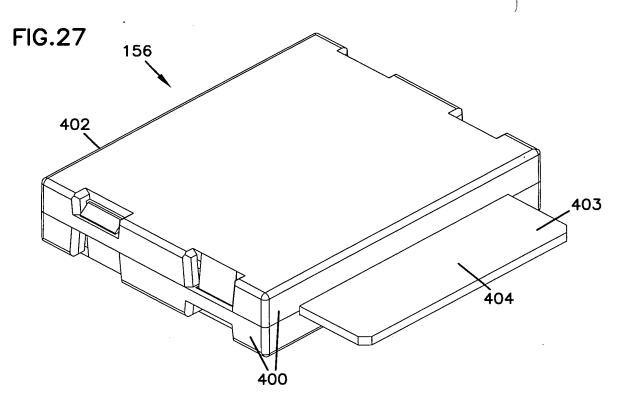
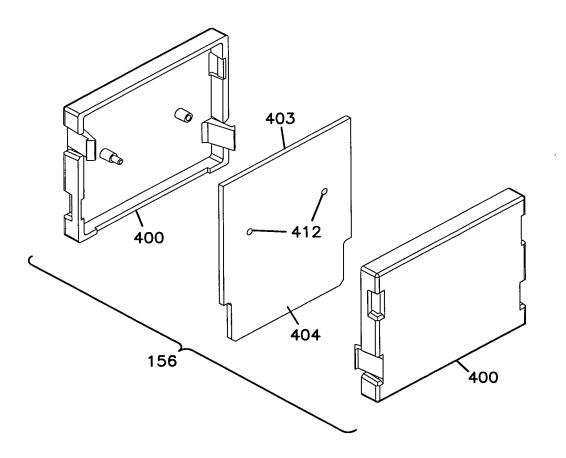


FIG.28



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FIG.29

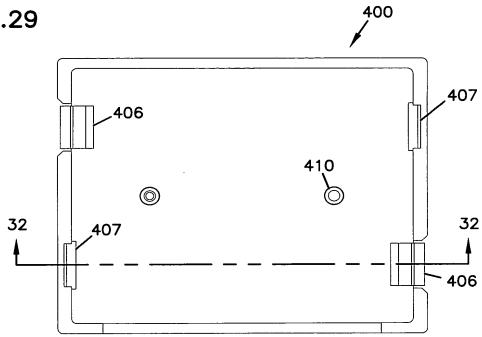
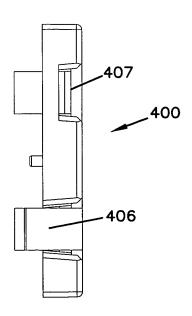


FIG.30 400 406 406

**FIG.31** 



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